

# Tera Term Terminal Emulator

## *Installation Guide*

UG1036 (v1.0.1) January 16, 2019



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## Revision History

The following table shows the revision history for this document.

Date	Version	Revision
01/16/2019	1.0.1	Updated Tera Term link.
02/12/2014	1.0	Initial Xilinx release.

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# Tera Term Terminal Emulator Installation Guide

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## Overview

This guide shows how to download and set up a Tera Term terminal emulator. The Tera Term emulator is used for demonstration purposes. Any other terminal emulator can be used, but Tera Term is recommended as it has been verified.

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## Download and Install Tera Term Terminal Emulator

1. Go to the [Tera Term](#) download page.
2. Accept the Tera Term license agreement and click the download arrow to download the executable file. See [Figure 1](#).

**Note:** Versions shown in figures might not match the latest version available.

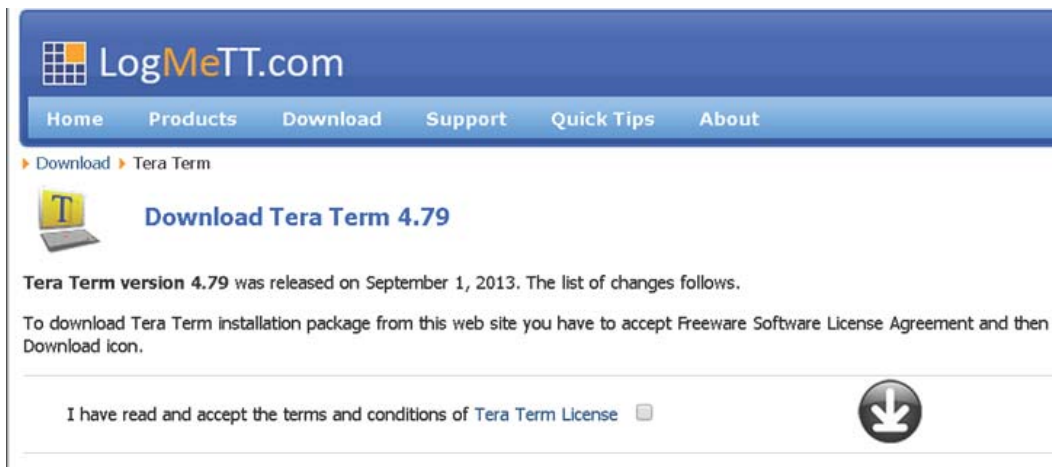


Figure 1: Tera Term Download Page

3. Open the .exe file and click **Run**. You might see some security warnings on your PC. Click **Run**, if applicable. The Tera Term Setup Wizard appears, as shown in [Figure 2](#).

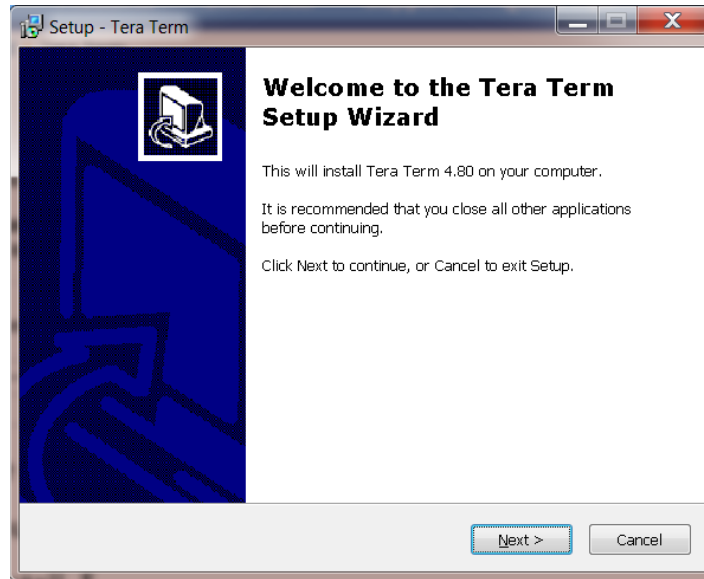


Figure 2: Tera Term Setup Wizard

4. Go through the setup. Agree to the license agreement and do not change any of the settings that appear in subsequent dialog boxes.
5. Click **Install**. Tera Term is installed and an icon is placed on your desktop.
6. Continue with the instructions in the appropriate quick start guide. At the appropriate time, you will be directed on the configuration of the terminal settings.

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## Xilinx Resources

For support resources such as Answers, Documentation, Downloads, and Forums, see [Xilinx Support](#).

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## Solution Centers

See the [Xilinx Solution Centers](#) for support on devices, software tools, and intellectual property at all stages of the design cycle. Topics include design assistance, advisories, and troubleshooting tips.

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## Documentation Navigator and Design Hubs

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- From the Vivado® IDE, select **Help > Documentation and Tutorials**.
- On Windows, select **Start > All Programs > Xilinx Design Tools > DocNav**.
- At the Linux command prompt, enter `docnav`.

Xilinx Design Hubs provide links to documentation organized by design tasks and other topics, which you can use to learn key concepts and address frequently asked questions. To access the Design Hubs:

- In the Xilinx Documentation Navigator, click the **Design Hubs View** tab.
- On the Xilinx website, see the [Design Hubs](#) page.

**Note:** For more information on Documentation Navigator, see the [Documentation Navigator](#) page on the Xilinx website.

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## References

These Xilinx documents provide supplemental material useful with this guide:

1. See the appropriate quick start guide for your Xilinx product.
2. *Silicon Labs CP210x USB-to-UART Installation Guide* ([UG1033](#))

These external websites provide supplemental material useful with this guide:

3. [CP210x USB-to-UART Bridge VCP Drivers](#)
4. [Tera Term](#) download page

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