

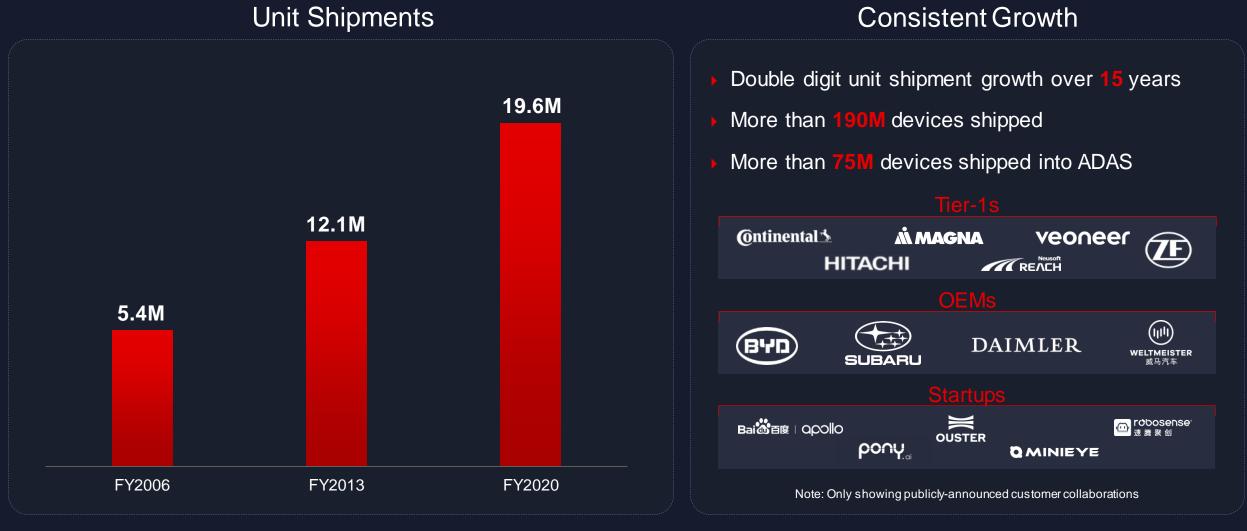
## Xilinx Adapt: Automotive

# Welcome

Mark Wadlington – SVP, CMG January 12<sup>th</sup>, 2021



### **Xilinx Steady Growth in Automotive**

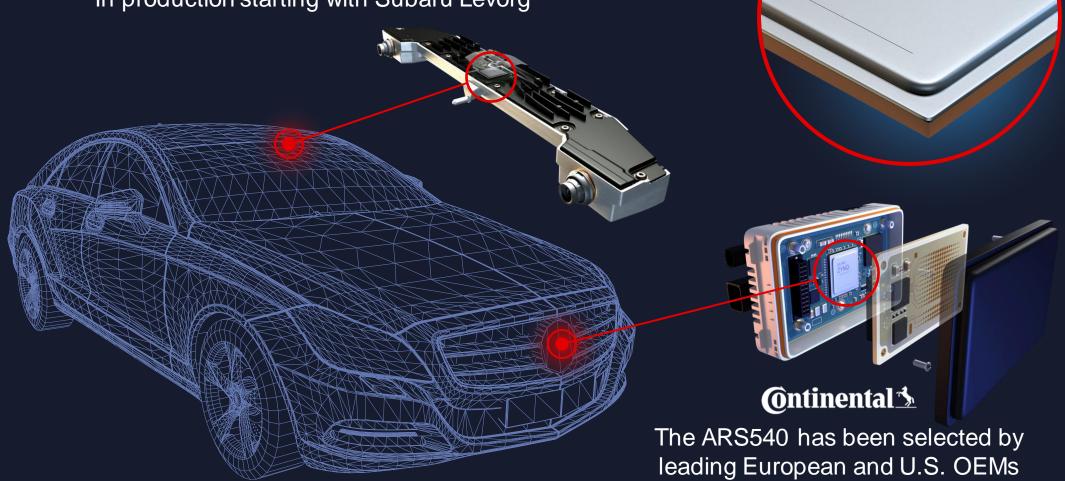


Production deployments with our 28nm and 16nm families to fuel continued growth

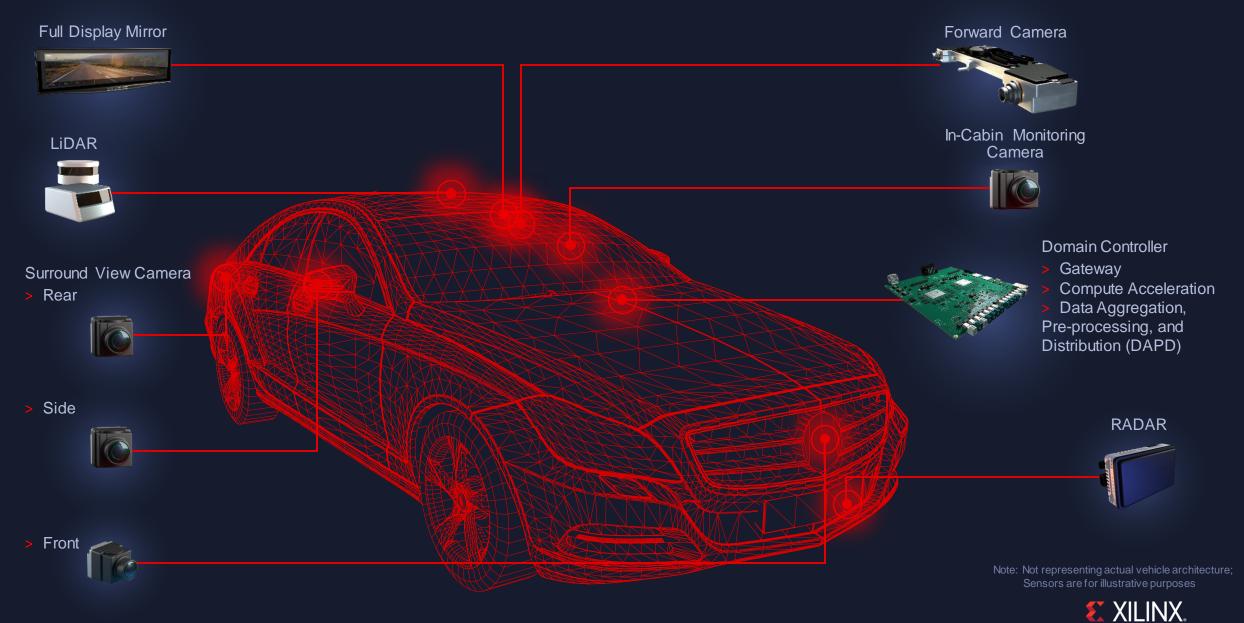




The New Generation EyeSight
In production starting with Subaru Levorg



### Xilinx Automotive ADAS & AD Focus Areas



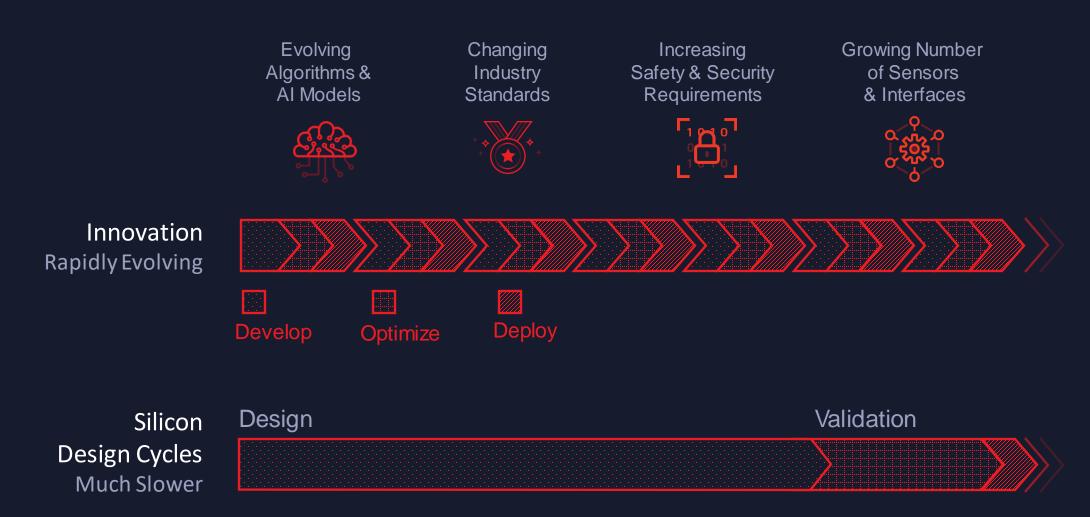
# Xilinx<sub>®</sub> Adapt



### **Fireside Chat**



### Innovation Cycles Outpacing Silicon Design Cycles

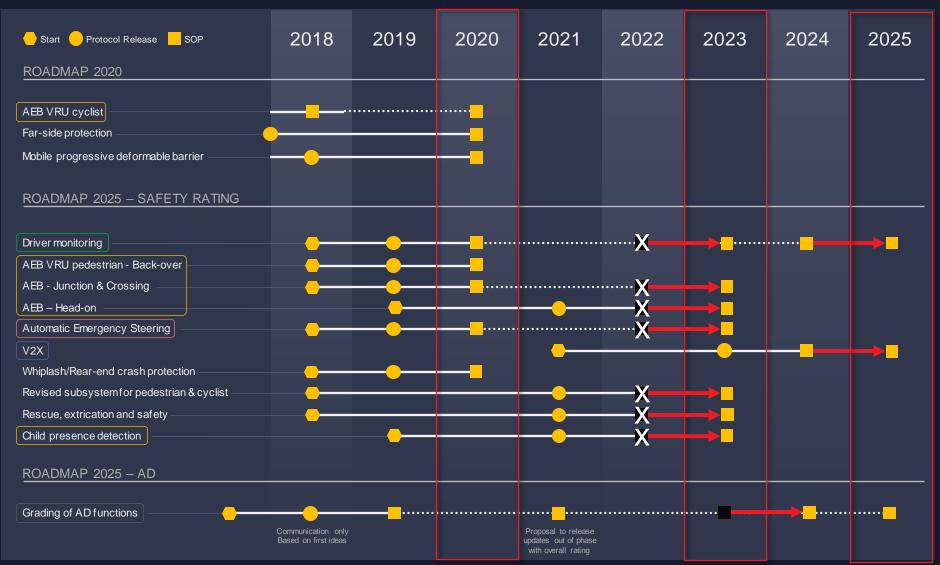


The World Needs Adaptive Compute ...



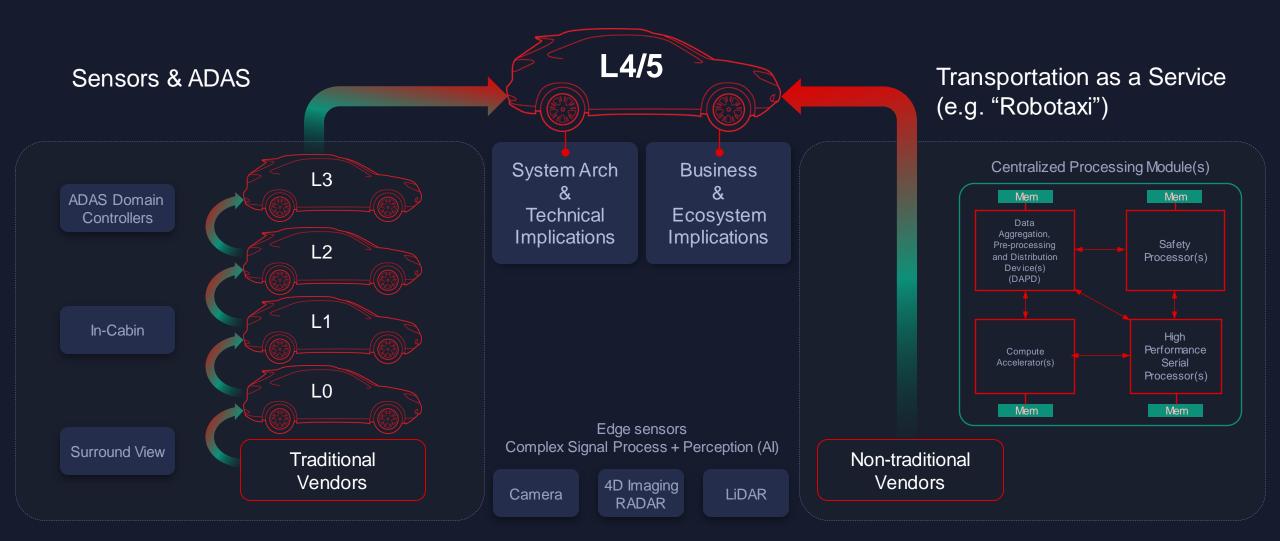
### Euro NCAP Roadmap 2020 – 2025

(Covid-19 Impact) - 2022 Requirements Delayed to 2023 and 2024 to 2025





# The Road to Autonomous Driving 2 types of Xilinx Customer: Evolutionary vs. Revolutionary



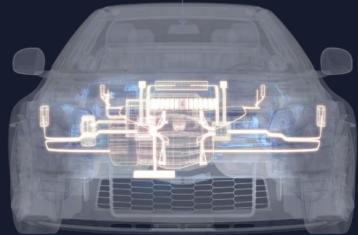


### Over-the-Air Silicon Updates (OTA)

- Future proof for emerging security threats
- Update safety algorithms
- Evolve neural network implementations over time
- Perform remediation or corrective action

Upgrade Hardware of Deployed Systems



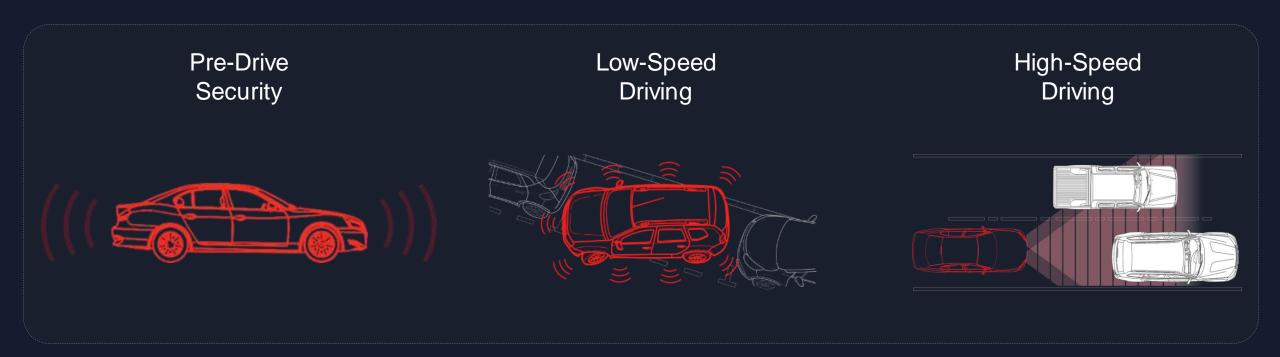




### **Dynamic Function eXchange (DFX)**

Dynamically Reconfigure Device to Reduce System-Wide Power and Cost

### **Swap Functionality in Milliseconds**





### Xilinx Automotive Role in Forward Camera Evolution

Xilinx Deployed in Production Systems for first 3 Generations and targets NCAP2022 with Next Generation of Devices

2008

2010

2012

2014

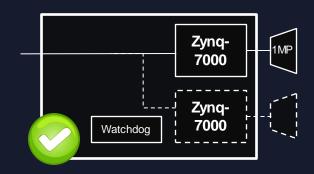
2016

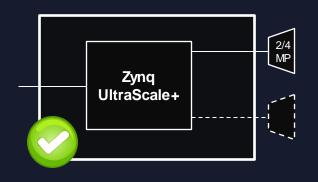
2018

2020

2022









#### GEN1: Spartan 6

- Camera: VGA/WVGA
- Warning Only, e.g. Lane
   Departure Warning
- Xilinx Value
  - Imager Interfacing
  - Image Conditioning and Feature Extraction

#### GEN2: Zynq 7000

- Camera: Up to 2 Mpixel
- Lane Departure Warning,
   Speed Alert, Collision
   Mitigation (AEB)
- Xilinx Value
  - Optimal HW/SW Partitioning
  - Scalability
  - Differentiation

#### GEN 3: Zynq MPSoC

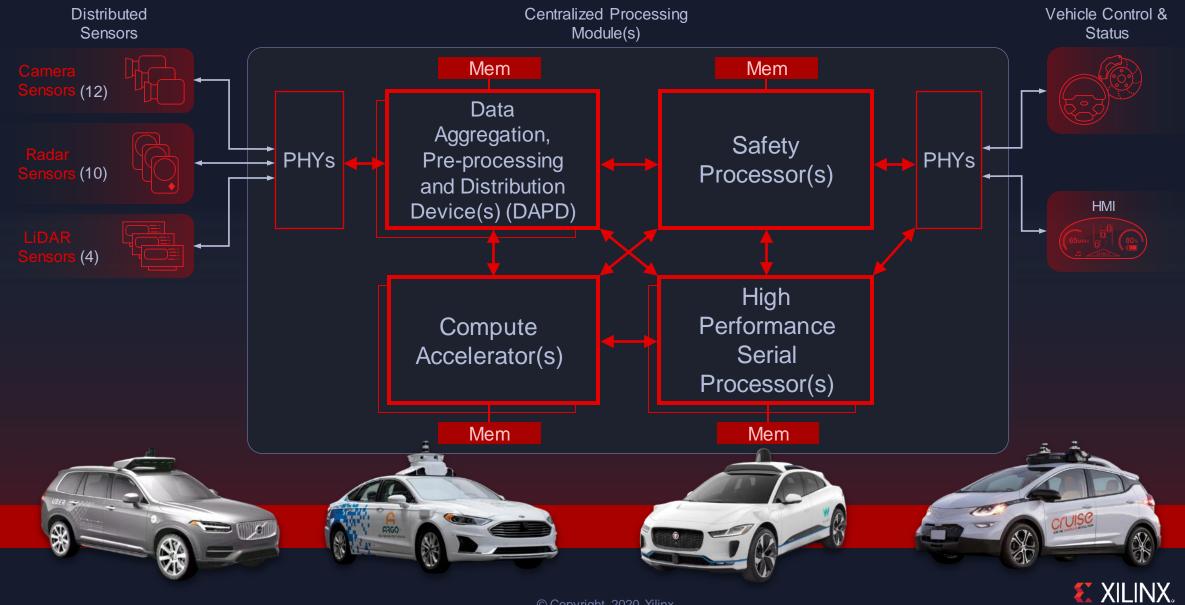
- Camera: Up to 4/8 Mpixel
- Broader Protection (e.g.
   Pedestrian/Cyclist Protection)
- Vehicle Convenience Control (e.g. Traffic Jam Assist)
- Xilinx Value
  - Heterogeneous processors
  - Tightly coupled Application SW and custom HW accelerators
  - Safety Island for FuSa

#### Future: ACAP

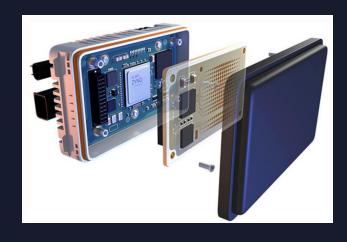
- Camera: Up to 8/12 Mpixel
- System Features:
- Level 2/3 Automation
- Urban and Highway Scenarios
- Xilinx Value
  - Higher Data Bandwidth Channels
  - High Performance / Low Power CNN Processing for environment Cognition
  - Advancing FuSa



## Adaptability and Scalability



### Xilinx Automotive in Radar & Lidar



Automotive Radar Module

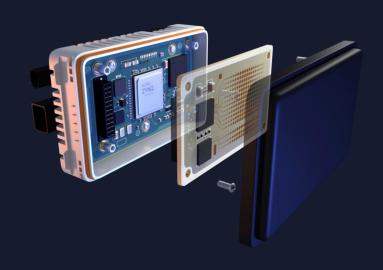


### **ML Processing on Lidar Data Demo**

- > Model: Pointpillars
- > Framework: Pytorch
- > Dataset: Kitti, 64-channel, 1~2Mpoints/sec
- > 25fps (40ms latency)



### Xilinx Automotive in Radar & Lidar



Automotive Radar Module



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### IVI Architectures with Xilinx Adaptable Extension

#### **South Bridge Connectivity**

- ▶ ADAS Sensor Expansion
- Specialized Display Drive

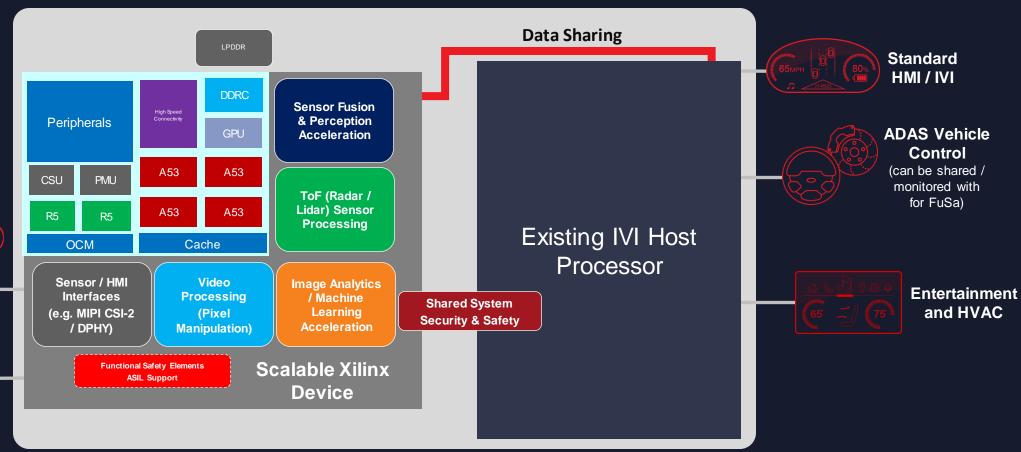
#### Feature Acceleration e.g.:

- DMS / ICMS
- Surround View / APA

Non-Standard HMI / IVI Displays



Radar Lidar Cameras





### Xilinx Automotive in Vehicle Electrification

