

Design Services Partner Profile



Fujitsu Limited Company Overview

Date : 09/22/2017

www.fujitsu.com/jp/qnet

Partner Details

Partner Tier: Certified
Headquarters Location: Fujitsu Kyushu R&D Center Bldg, 2-1 Momochihama 2-chome, Sawara-ku, Fukuoka,, Japan
Xilinx Alliance Partner Since: 6/9/2011

Key Contact Info

Hikida Kouji
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Regions Served

Supported Product Types

Yes	Design Services	No	Embedded Products
No	IP	No	EDA Tools
No	Boards		

Engineering Service Types

Yes	General Purpose Services	Preferred
No	Services in Support of Products Sold	
No	Services Leading to Contract Manufacturing	
Maximum Service Scope:		Chip level FPGA / ASIC design

Engagement Models Supported

Yes	SOW-Based Fixed Bid Projects	Preferred
No	Time and Materials	
None	Captive Resources at Partner or Customer Premises	

Xilinx Design Tool Experience

Yes	Vivado	No	SDSoC
Yes	HLx	No	SDAccel

Industry Certifications Achieved

ISO 9001

Industry Certification Services Offered

Government Clearances

None

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Fujitsu Limited Segment and Application Focus

No Focus	Defense/Aerospace	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus No Focus No Focus No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Homeland Security Electronic Warfare ISR Space Missiles & Munitions Military Avionics Commercial Avionics SatComm Wireless Milcom Other A&D
Primary Focus	Audio, Video, Broadcast	<ul style="list-style-type: none"> No Focus No Focus Primary Focus No Focus No Focus No Focus Primary Focus Secondary Focus Secondary Focus 	<ul style="list-style-type: none"> Encoders & Decoders Monitors & Projection Networking & Converters Audio Systems Video Processing Cards Video Conferencing Routers and Switches Pro Cameras & Camcorders Other Broadcast
Primary Focus	Automotive & Transport	<ul style="list-style-type: none"> Primary Focus No Focus No Focus No Focus Secondary Focus 	<ul style="list-style-type: none"> Driver Assist - Camera Driver Assist Radar/LIDAR Driver Info/Infotainment Automated Driving Other Automotive
No Focus	Consumer	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Digital Display Drones Multifunction Printers Other Consumer
No Focus	Datacenter	<ul style="list-style-type: none"> No Focus No Focus No Focus 	<ul style="list-style-type: none"> Storage Compute Networking
Secondary Focus	Industrial, Medical	<ul style="list-style-type: none"> Secondary Focus Secondary Focus Secondary Focus Secondary Focus No Focus Primary Focus Secondary Focus 	<ul style="list-style-type: none"> Motor Control Industrial Networking Surveillance Machine Vision Medical Imaging Medical / Clinical Other ISM

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Fujitsu Limited Segment and Application Focus (continued)

Secondary Focus	Test & Measurement	<ul style="list-style-type: none"> Primary Focus No Focus Secondary Focus No Focus No Focus Secondary Focus 	<ul style="list-style-type: none"> Simulation / Emulation T & M Instrumentation Semiconductor Test Wired Communication Test Wireless Communications Test Other Test & Measurement
Primary Focus	Wired Comms & Networks	<ul style="list-style-type: none"> No Focus No Focus Primary Focus Primary Focus Primary Focus No Focus No Focus Secondary Focus 	<ul style="list-style-type: none"> Access Equipment Long Haul/Transport Wired Mobile Backhaul OTN Enterprise Equipment Ethernet Interlaken Connectivity Packet Processing Other Wired Communications
Secondary Focus	Wireless Comms & Networks	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Baseband Wireless Backhaul Connectivity & Switching Radio Other Wireless

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Design Competencies

System Architecture	No Focus	
RF and Analog Design	No Focus	
FPGA Design and Integration	Primary Focus	[experience] <ul style="list-style-type: none">• We developed and integrated Multi-die UltraScale, 4 designs.• We developed Virtex-6 with Partial Reconfiguration, 2 designs.• High Speed /High Density FPGA Design (100G/400G Ethernet/OTN , 160G I
FPGA Timing Closure	Primary Focus	[experience] <ul style="list-style-type: none">• over 300MHz Timing Closure for UltraScale [service] <ul style="list-style-type: none">• Design service for Timing Closure consultant
FPGA Design Optimization	Primary Focus	[experience] <ul style="list-style-type: none">• over 300MHz Timing Closure for UltraScale• Power reduce design for UltraScale/Virtex-7• Pin/Clocking optimization for Multi-die FPGAs [service] <ul style="list-style-type: none">• Design service for Timing Closure consultant• De
Signal Integrity Design and Analysis	No Focus	Inhouse or Subcontracted: Typical Serial Speed: 0
PCB Design & Layout	No Focus	Inhouse or Subcontracted:
Industrial and Mechanical Design	No Focus	Inhouse or Subcontracted:
Industry Certification and Compliance Testing	No Focus	Inhouse or Subcontracted:

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Quality and Reliability No Focus

Testing

Inhouse or Subcontracted:

Contract Manufacturing No Focus

Inhouse or Subcontracted:

Max Quantities Supported:

Digital Signal Processing No Focus

Experience:

Embedded Processors No Focus

Experience:

Embedded Porting / No Focus

BSP Extension

OS and RTOS Design Experience:

Embedded Application No Focus

Development

Image Sensor Fusion

Homo or Heterogeneous:

Sensor Types Design Experience:

Computer Vision

Machine Learning

Framework Experience:

Network Model Experience:
