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Xylon Unveils New Panoramic Multi-Sensor Camera Demo at Xilinx Developer Forum

The new 360 degrees panoramic camera demo from Xylon showcases simultaneous video processing of 2 to 6 multi-megapixel camera sensors that can be upgraded by AI algorithms and implemented in a single Xilinx Zynq UltraScale+ MPSoC

SAN JOSE, Calif., Oct. 2, 2018 (Xilinx Developer Forum) – Xylon, a leading provider of logicBRICKS intellectual property (IP) cores, design services and solutions for Xilinx FPGA and SoC devices, has announced a new Panoramic Multi-Sensor Camera Demo at the Xilinx Developer Forum (XDF) 2018. XDF connects software developers and system designers to the deep expertise of Xilinx engineers, partners, and industry leaders.

Based on a flexible hardware platform that uses powerful Xilinx Zynq UltraScale+ MPSoC designed by versatile Xylon logicBRICKS IP cores, the new Panoramic Multi-Sensor Camera Demo demonstrates simultaneous video processing from 2 to 6 multi-megapixel camera sensors and stitching of an integrated high-resolution image to assure full coverage of wide area. The camera design can be upgraded with Xylon and third-party artificial intelligence IP cores to detect and recognize activities of people and various objects, which with the total situational awareness, enables its use in wide range of surveillance, defense, AR and VR, and other applications.

"In the past Xylon had already designed multi-sensor surveillance cameras, including sensor fusion cameras that combine optical and thermal sensors." said Davor Kovacec, founder and CEO of Xylon. "Based on the gained experience, we truly believe that the Xilinx Zynq UltraScale+ MPSoC offers the right features blend for the next-generation surveillance cameras."

Several Xylon logicBRICKS IP cores have been used in the Panoramic Camera demo design, including the key IP core for the Xylon Surround View Parking Assistance ADAS solutions – the logiVIEW Perspective Transformation and Lens Correction Image Processor IP core. The logiVIEW IP core removes lens distortions and distortions at the stitching lines, corrects camera perspectives, scales the images and generates the stitched 360 degree video output. Xylon delivers the demo with the Linux software application for hand-calibration that can be upgraded with an automatic calibration system.

"Xylon's new panoramic camera demo clearly shows how fast and easy the Xilinx devices combined with the right reusable intellectual property can enable custom SoCs tuned for emerging video and vision applications," said Chris Dunlap, director of ecosystems, Xilinx. "With the right hardware in hand at the early stage of a project, software developers can rapidly develop a complete smart multi-camera video and vision system."

More information about the logiVIEW IP core is available at: https://www.logicbricks.com/Products/logiVIEW.aspx.

For more information about the Panoramic Multi-Sensor Camera Demo, lead times and deliverables, please visit booth #26 at the XDF exhibition, or contact Xylon at info@logicbricks.com.

More information about XDF is available at: https://www.xilinx.com/products/design-tools/developer-forum/silicon-valley.html. For additional updates from XDF, follow Xilinx on Twitter at @XilinxInc or via the hashtag #XDF2018.

About Xylon

Xylon is an electronic company focused on design of optimized logicBRICKS IP cores for Xilinx All Programmable devices and design services that lower production costs and improve efficiency of electronics designers. The company was founded in 1995, and since then it has grown into a prominent provider of intellectual property in the fields of embedded graphics, video and vision. Xylon is a Premier Member of the Xilinx Alliance Program. More information can be found at www.logicbricks.com, the official Xylon web site.

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